Docket No. 34-002
Lee et al.

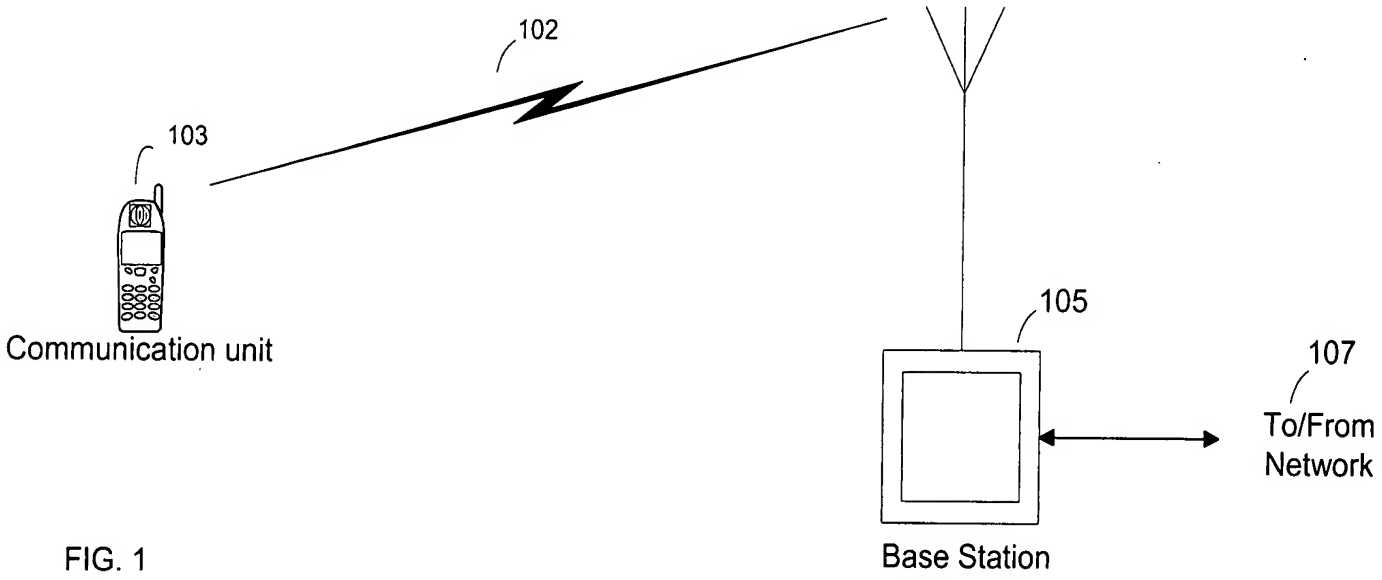


FIG. 1

Docket No. 34-002
Lee et al.

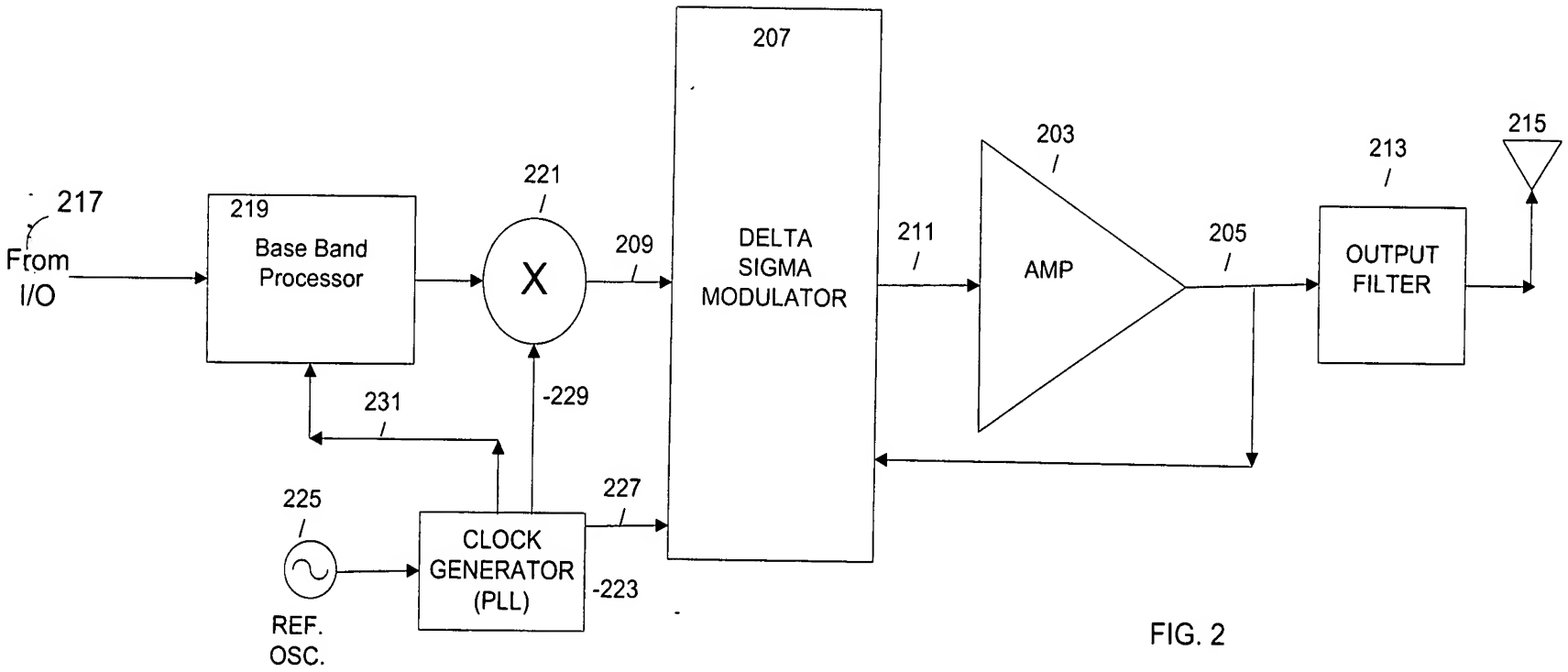


FIG. 2

Docket No. 34-002
Lee et al.

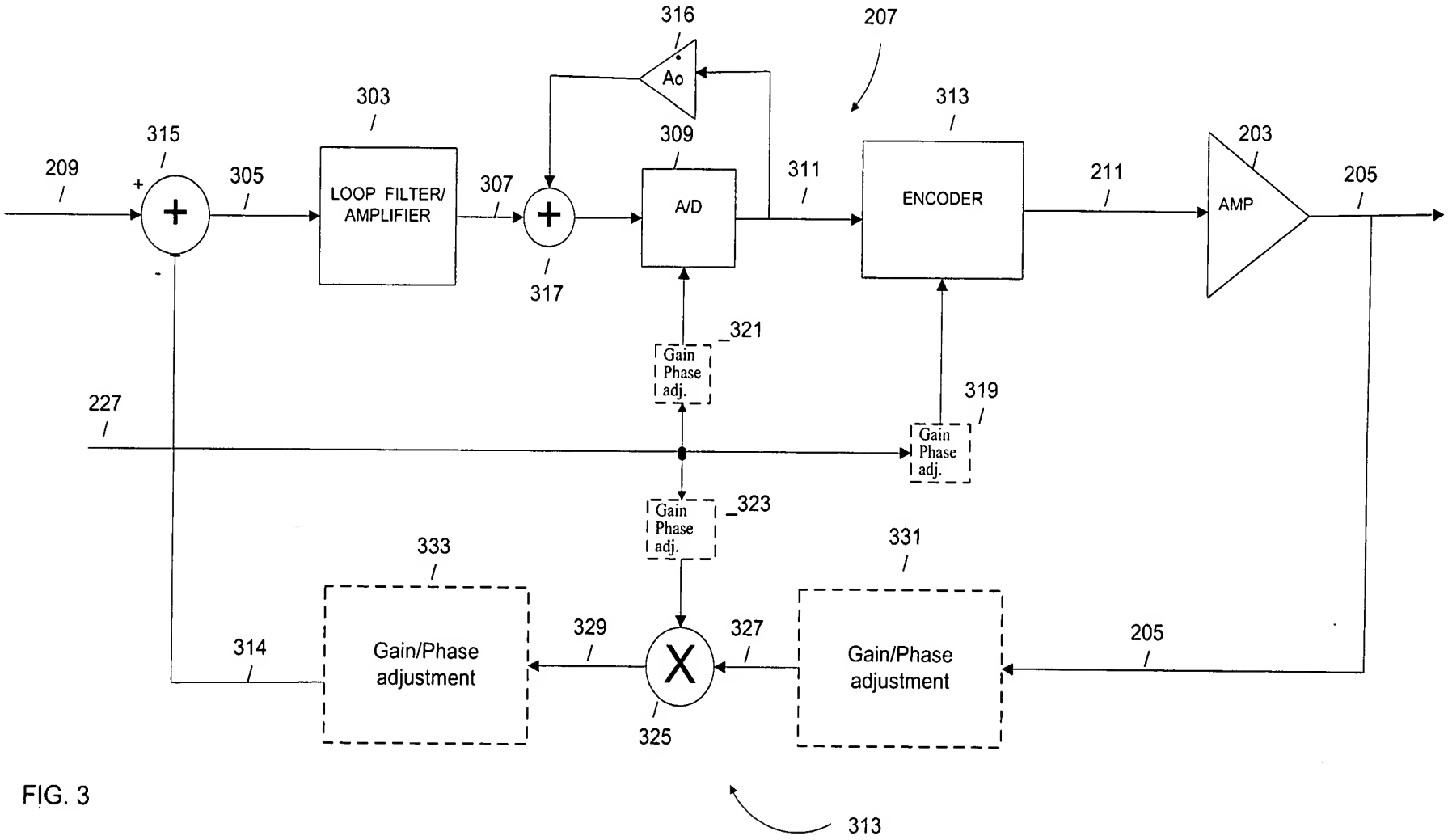


FIG. 3

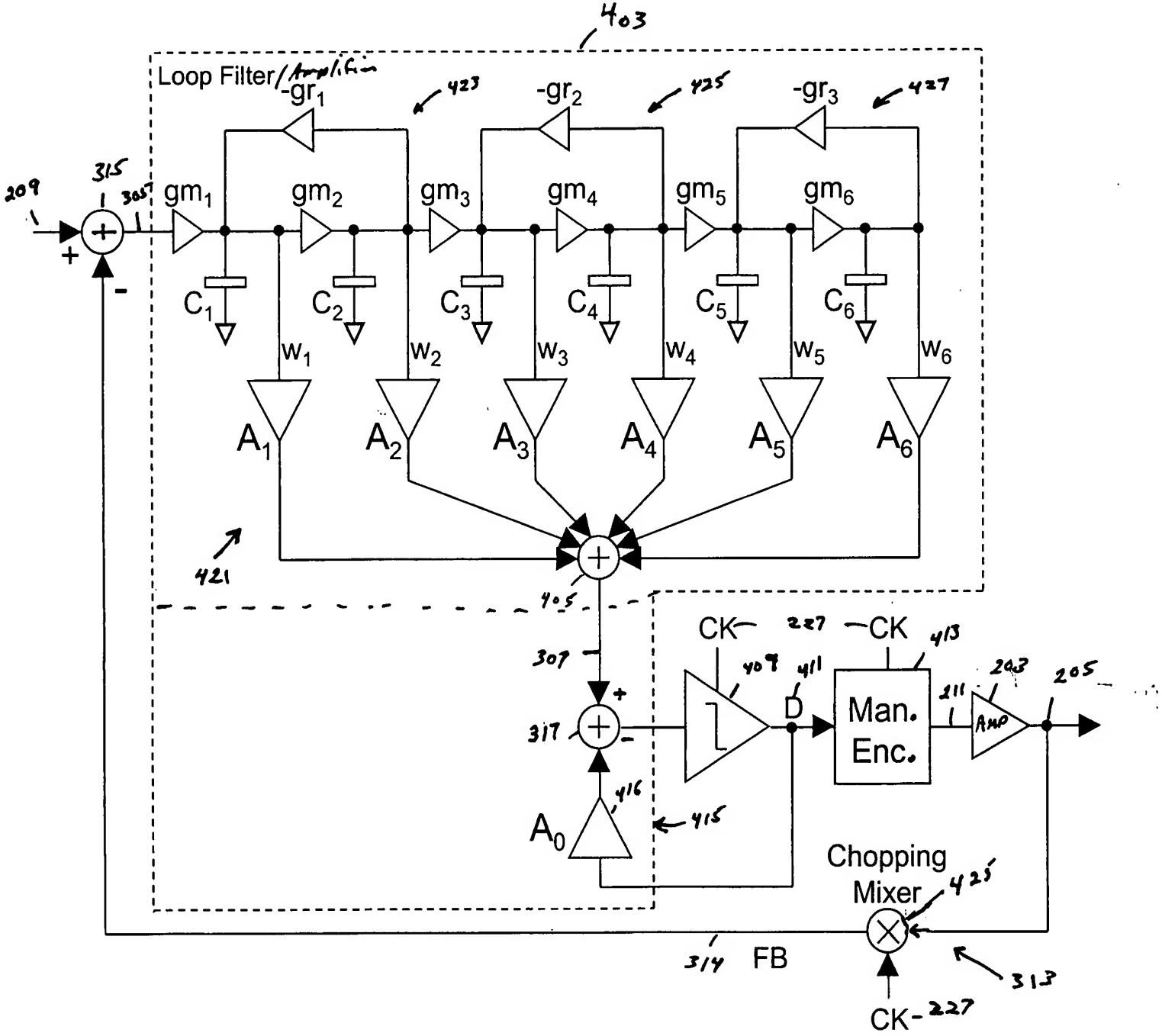
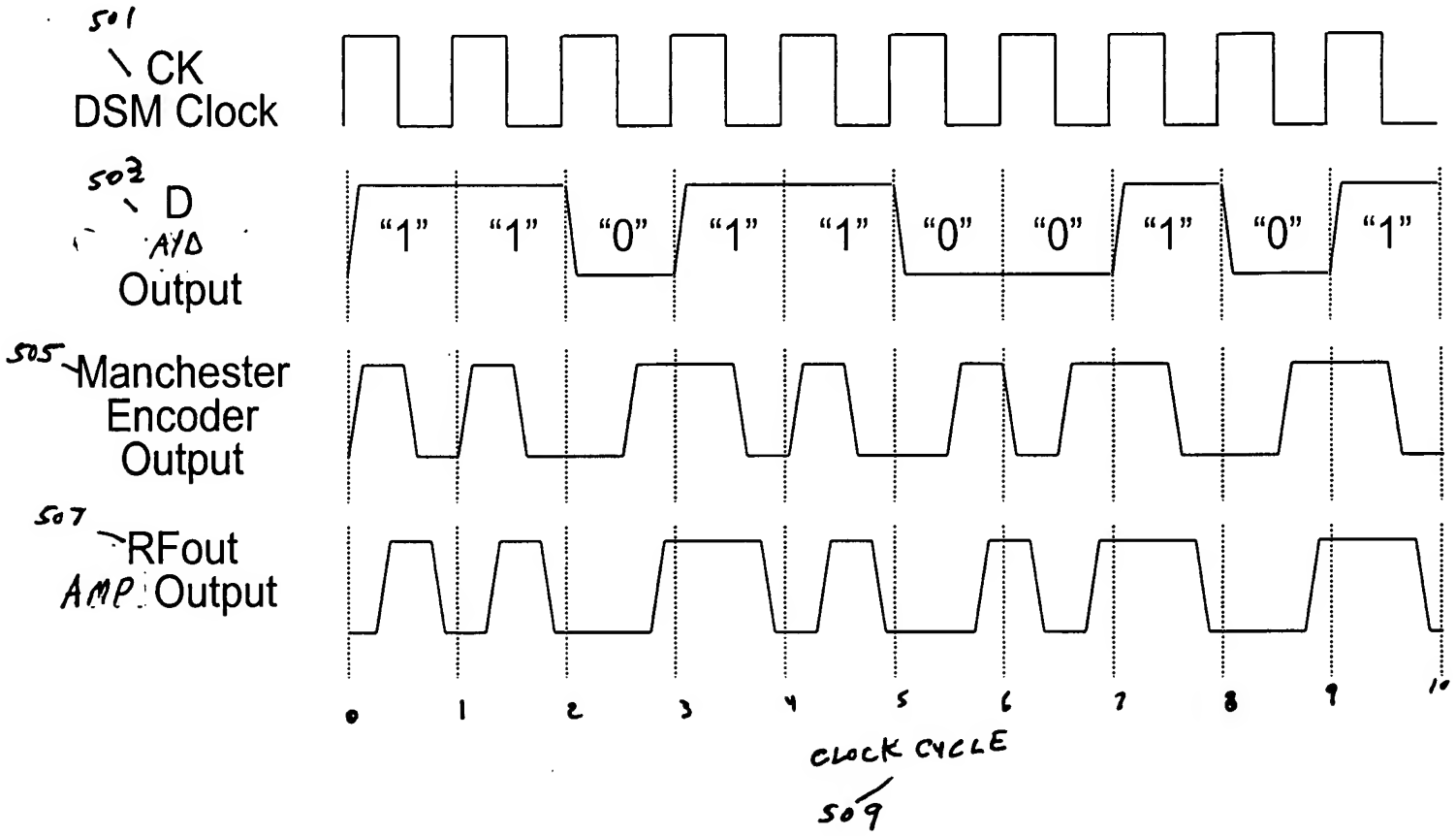


FIG. 4 34-002 Lee et al.

Sheet 4/7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CK
DSM Clock
502
D
A/D
Output
"1" "1" "0" "1" "1" "0" "0" "1" "0" "1"
505
Manchester
Encoder
Output
507
RFout
AMP Output
0 1 2 3 4 5 6 7 8 9 10
CLOCK CYCLE
509

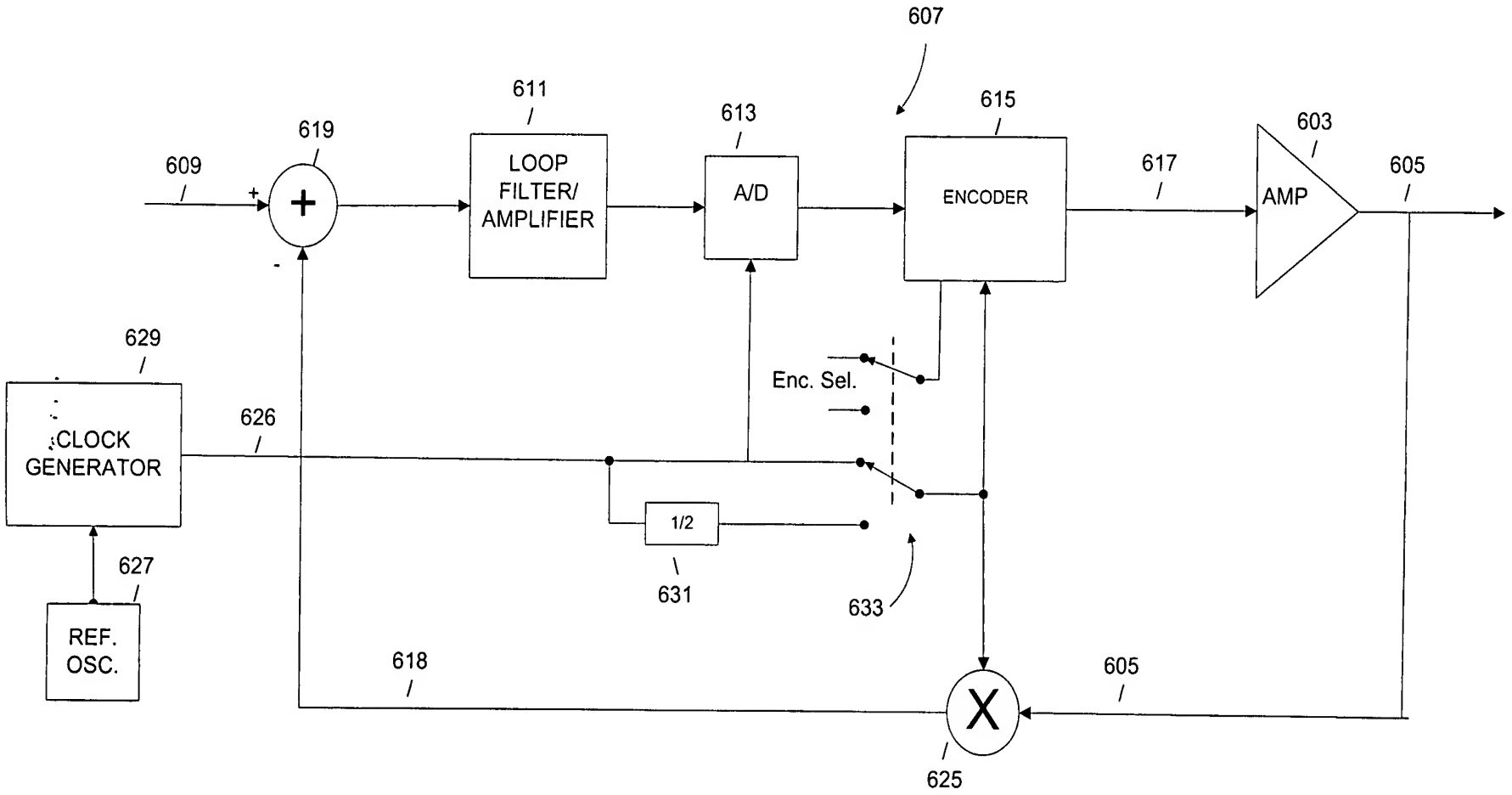
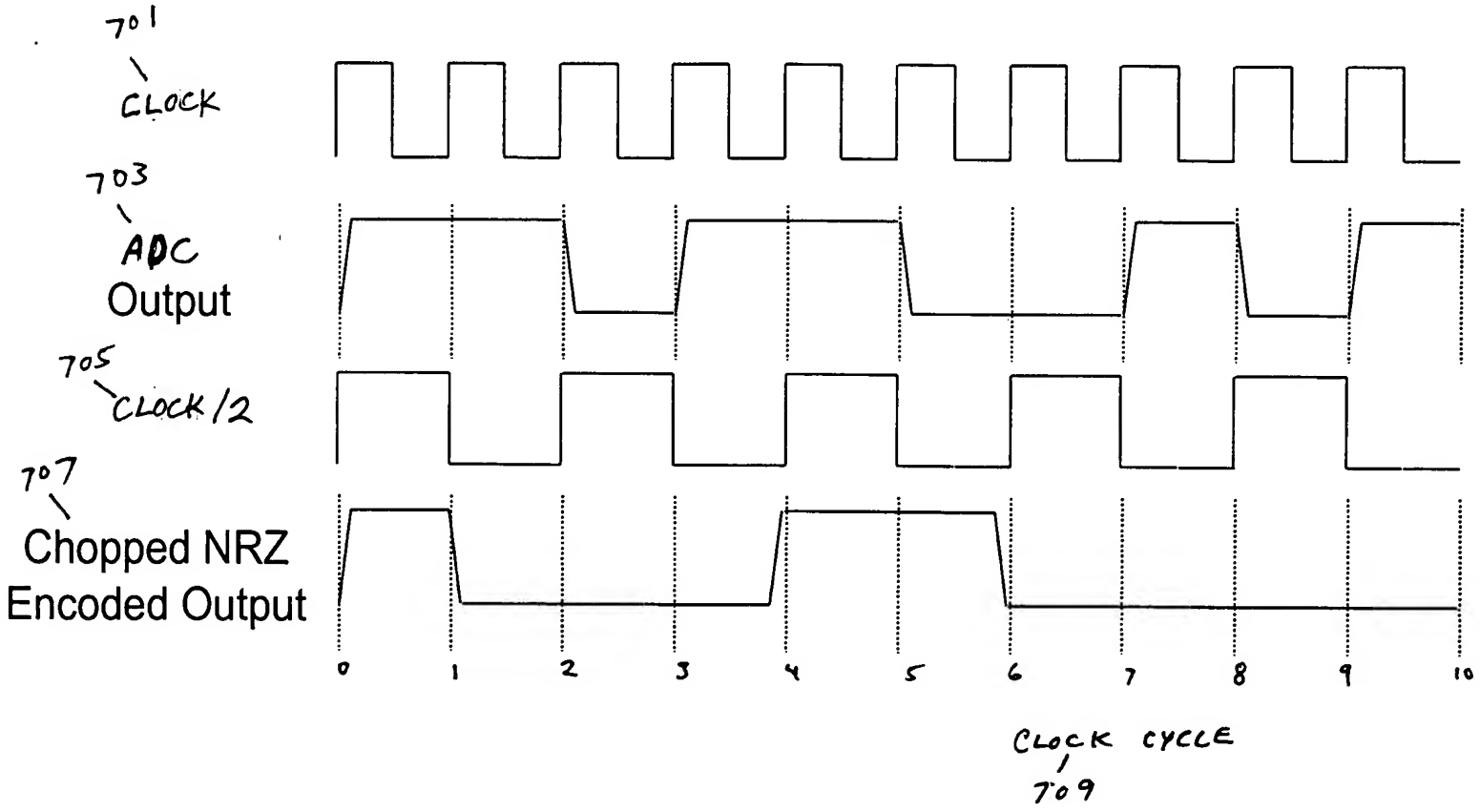

607
611
613
615
603
619
609
LOOP FILTER/ AMPLIFIER
A/D
ENCODER
617
AMP
605
629
Enc. Sel.
626
CLOCK GENERATOR
1/2
631
633
627
REF. OSC.
618
605
625

FIG. 6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CLOCK
703
ADC
Output
705
CLOCK/2
707
Chopped NRZ
Encoded Output
0
1
2
3
4
5
6
7
8
9
10
CLOCK CYCLE
709